

Lab 2: Cadence Tutorial on Layout and DRC/LVS/PEX

ICM Documentation

The IBM design kits include many reference documents available in PDF format. You can access the most relevant of these from the Cadence Virtuoso® CFW window. Select IBM_PDK > Document > Design Manual. Under "Technology" select *cmos18f* and click ok. A PDF document should open called the Design Rule Manual (DRM). If this does not work, the DRM can be accessed directly by typing the following in a terminal window, where "%>" represents the terminal window prompt (i.e. type the text after the "%>").

```
%> moretail -f /nexus4 IBM_PDK/cmu18f/DRM/1.8.0.2 IBM/doc/cm18f/Design_Manual.pdf
```

This document contains all of the electrical properties of the devices in the process. It also contains layout design rules and other information about the process. You can find a *cross* section of the process on page 28.

Introduction

This tutorial describes how to generate a layout view in the Cadence Virtuoso Layout Editor, how to perform layout verification in Calibre, and how to re-simulate your design with extracted parasitics in Specus. In the first section, you will generate a layout for a simple CMOS inverter. In the second, you will create a buffer using the inverter cell you have created.

When doing layout, a techfile must be attached to the library. This defines layers, parameterized cells (pcells) and other layout-related technology information. This should have been set in Lab 1, but we will double check the techfile. To do this, start Virtuoso in your *cadence* directory.

```
%> cd ~/nexus4 IBM  
%> module load /afs/wnl.cmu.edu/.classes/Specus/modules/spec4.2.0/015  
%> virtuoso &
```

Open the Library Manager from the CFW by browsing to

Tools > Library Manager

Right click on the tutorial library that was created in tutorial session 1 and choose *Properties*. The *techfilebase* field should read *cmos18f*. If it does not, enter *cmos18f* in the textbox for *techfilebase* and click on *OK*. You have now attached the techfile. In the list of cells, click on *cmos_inverter* (the name you chose for your inverter in Lab 1), then choose File > New > Cell View.

Cadence Lab Manual

IM Harris

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[Microbiology, Laboratory Manual and Workbook](#) Alice Lorraine Smith, 1981

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