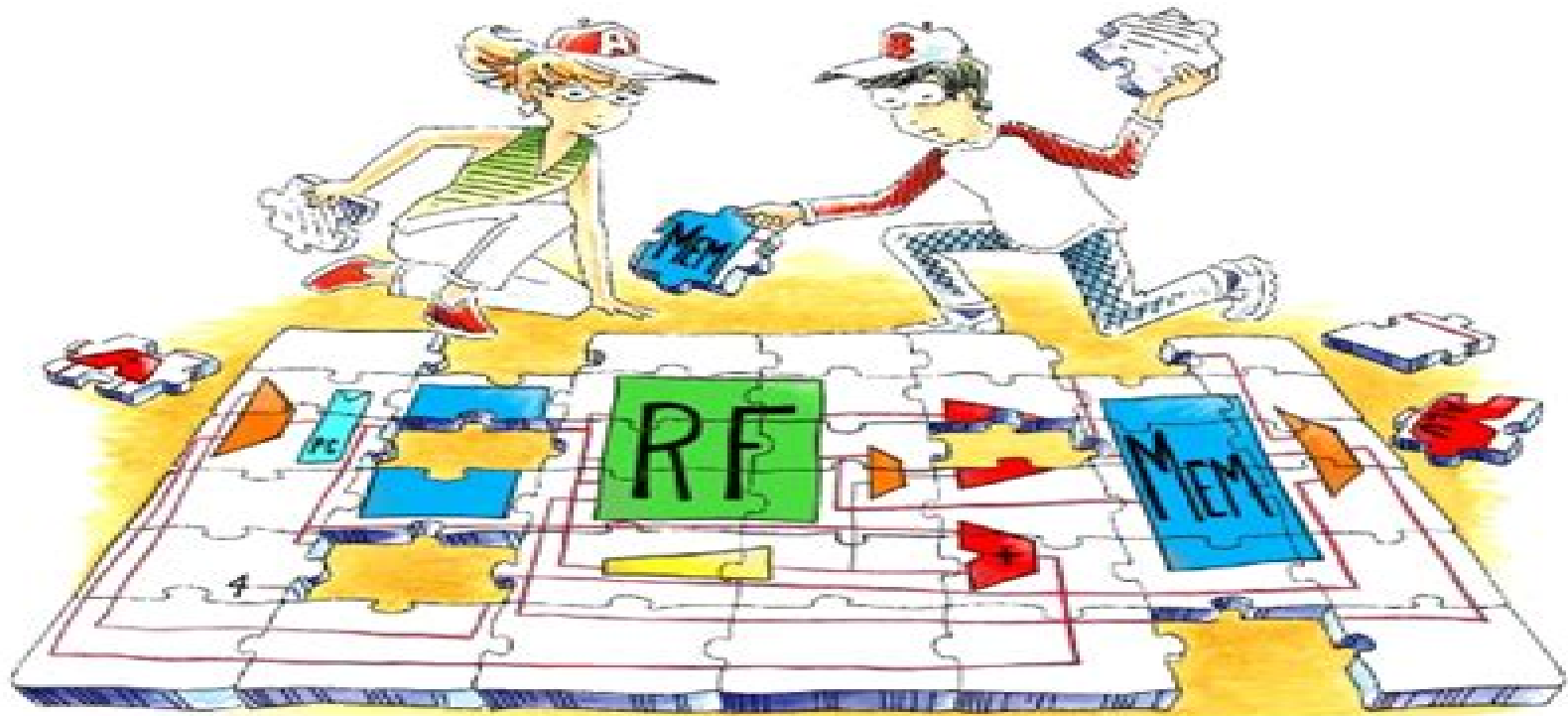


# Digital Design and Computer Architecture

## RISC-V Edition



# Digital Design And Computer Architecture Digital Design And Computer Architecture

**Edward Karalis**



## **Digital Design And Computer Architecture Digital Design And Computer Architecture:**

Digital Design and Computer Architecture David Harris, Sarah Harris, 2012-08-24 Digital Design and Computer Architecture Second Edition takes a unique and modern approach to digital design introducing the reader to the fundamentals of digital logic and then showing step by step how to build a MIPS microprocessor in both Verilog and VHDL. This new edition combines an engaging and humorous writing style with an updated and hands on approach to digital design. It presents new content on I/O systems in the context of general purpose processors found in a PC as well as microcontrollers found almost everywhere. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, the book uses these fundamental building blocks as the basis for the design of an actual MIPS processor. It provides practical examples of how to interface with peripherals using RS232, SPI, motor control, interrupts, wireless, and analog-to-digital conversion. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. There are also additional exercises and new examples of parallel and advanced architectures, practical I/O applications, embedded systems, and heterogeneous computing, plus a new appendix on C programming to strengthen the connection between programming and processor architecture. This new edition will appeal to professional computer engineers and to students taking a course that combines digital logic and computer architecture. Updated based on instructor feedback with more exercises and new examples of parallel and advanced architectures, practical I/O applications, embedded systems, and heterogeneous computing. Presents digital system design examples in both VHDL and SystemVerilog, updated for the second edition from Verilog, shown side by side to compare and contrast their strengths. Includes a new chapter on C programming to provide necessary prerequisites and strengthen the connection between programming and processor architecture. Companion Web site includes links to Xilinx CAD tools for FPGA design, lecture slides, laboratory projects, and solutions to exercises. Instructors can also register at [textbooks.elsevier.com](http://textbooks.elsevier.com) for access to Solutions to all exercises, PDF Lab materials with solutions, HDL for textbook examples and exercise solutions, Lecture slides, PPT, Sample exams, Sample course syllabus, Figures from the text, JPG, PPT.

**Digital Design and Computer Architecture, RISC-V Edition** Sarah Harris, David Harris, 2021-07-12 The newest addition to the Harris and Harris family of Digital Design and Computer Architecture books, this RISC-V Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC-V microprocessor. Combining an engaging and humorous writing style with an updated and hands-on approach to digital design, this book takes the reader from the fundamentals of digital logic to the actual design of a processor. By the end of this book, readers will be able to build their own RISC-V microprocessor and will have a top-to-bottom understanding of how it works. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, this book uses these fundamental building blocks as the basis for designing a RISC-V processor. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques

for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems

**Digital Design and Computer Architecture** David Harris,Sarah Harris,2010-07-26 Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material

*Digital Design and Computer Architecture* David Harris,Sarah Harris,2015-04-22 Digital Design and Computer Architecture ARM Edition takes a unique and modern approach to digital design Beginning with digital logic gates and progressing to the design of combinational and sequential circuits Harris and Harris use these fundamental building blocks as the basis for what follows the design of an actual ARM processor With over 75% of the world s population using products with ARM processors the design of the ARM processor offers an exciting and timely application of digital design while also teaching the fundamentals of computer architecture SystemVerilog and VHDL

are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design By the end of this book readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works Harris and Harris have combined an engaging and humorous writing style with an updated and hands on approach to digital design Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Features side by side examples of the two most prominent Hardware Description Languages HDLs SystemVerilog and VHDL which illustrate and compare the ways each can be used in the design of digital systems Includes examples throughout the text that enhance the reader s understanding and retention of key concepts and techniques The Companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors The Companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises

Digital Design and Computer Architecture David Money Harris, Sarah L. Harris, 2015      **Digital Design and Computer Architecture** David Maney Harris, Sarah L. Harris, 2007      Digital Design and Computer Organization Hassan A. Farhat, 2003-12-29 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits The book includes an accompanying CD that includes the majority of circuits highlighted in the text delivering you hands on experience in the simulation and observation of circuit functionality These circuits were designed and tested with a user friendly Electronics Workbench package Multisim Textbook Edition that enables your progression from truth tables onward to more complex designs This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles

**Computer Systems** Ata Elahi, 2017-11-08 This textbook covers digital design fundamentals of computer architecture and assembly language The book starts by introducing basic number systems character coding basic knowledge in digital design and components of a computer The book goes on to discuss information representation in computing Boolean algebra and logic gates sequential logic input output and CPU performance The author also covers ARM architecture ARM instructions and ARM assembly language which is used in a variety of devices such as cell phones digital TV automobiles routers and switches The book contains a set of laboratory experiments related to digital design using Logisim software in addition each chapter features objectives summaries key terms review questions and problems The book is targeted to students majoring Computer Science Information System and IT and follows the ACM IEEE 2013 guidelines Comprehensive textbook covering digital design computer architecture and ARM architecture and assembly Covers basic number system and coding basic knowledge in digital design and components of a computer Features

laboratory exercises in addition to objectives summaries key terms review questions and problems in each chapter

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**Digital Design And Computer Architecture** David Money Harris,2009-01-01  
*Digital Design and Computer Organisation* D. Nasib S. Gill,J.B. Dixit,2008-12 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits The book includes an accompanying CD that includes the majority of circuits highlighted in the text delivering you hands on experience in the simulation and observation of circuit functionality These circuits were designed and tested with a user friendly Electronics Workbench package Multisim Textbook Edition that enables your progression from truth tables onward to more complex designs This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles

**Digital Design Principles and Computer Architecture** Edward Karalis,1997 Appropriate for courses in Digital Design Microprocessor Based Design Computer Architecture Computer Communications and Networks and Analog and Mixed Signal Design found in 2 year and 4 year electronics technology and engineering departments Based on the author s 20 years of hands on experience this text articulates the underlying principles and practices that comprise today s digital components and systems Karalis provides a comprehensive yet concise fully integrated treatment of relevant analytical tools and design methodologies as applied to various integrated devices modules systems and signal interactions Coverage offers a balance of theory application and functional level examples

**Digital Design and Computer Architecture, RISC-V Edition** Sarah L. Harris,David Money Harris,2025

**Digital Design Principles and Computer Architecture** KARALIS,1996-07-01

**Digital Arithmetic** Milos D. Ercegovac,Tomás Lang,2004 The authoritative reference on the theory and design practice of computer arithmetic

**Complete Digital Design: A Comprehensive Guide to Digital Electronics and Computer System Architecture** Mark Balch,2003-06-22 YOUR ONE STOP RESOURCE FOR DIGITAL SYSTEM DESIGN The explosion in communications and

embedded computing technologies has brought with it a host of new skill requirements for electrical and electronics engineers students and hobbyists With engineers expected to have such diverse expertise they need comprehensive easy to understand guidance on the fundamentals of digital design Enter McGraw Hill s Complete Digital Design Written by an experienced electrical engineer and networking hardware designer this book helps you understand and navigate the interlocking components architectures and practices necessary to design and implement digital systems It includes Real world implementation of microprocessor based digital systems Broad presentation of supporting analog circuit principles Building complete systems with basic design elements and the latest technologies Complete Digital Design will teach you how to develop a customized set of requirements for any design problem and then research and evaluate available components and technologies to solve it Perfect for the professional the student and the hobbyist alike this is one volume you need handy at all times What you ll find inside Digital logic and timing analysis Integrated circuits Microprocessor and computer architecture Memory technologies Networking and serial communications Finite state machine design Programmable logic CPLD and FPGA Analog circuit basics Diodes transistors and operational amplifiers Analog to digital conversion Voltage regulation Signal integrity and PCB design And more

*Digital Design and Implementation with Field Programmable Devices* Zainalabedin Navabi, 2006-02-28 This book is on digital system design for programmable devices such as FPGAs CPLDs and PALs A designer wanting to design with programmable devices must understand digital system design at the RT Register Transfer level circuitry and programming of programmable devices digital design methodologies use of hardware description languages in design design tools and environments and finally such a designer must be familiar with one or several digital design tools and environments Books on these topics are many and they cover individual design topics with very general approaches The number of books a designer needs to gather the necessary information for a practical knowledge of design with field programmable devices can easily reach five or six much of which is on theoretical concepts that are not directly applicable to RT level design with programmable devices The focus of this book is on a practical knowledge of digital system design for programmable devices The book covers all necessary topics under one cover and covers each topic just enough that is actually used by an advanced digital designer In the three parts of the book we cover digital system design concepts use of tools and systematic design of digital systems In the first chapter design methodologies use of simulation and synthesis tools and programming programmable devices are discussed Based on this automated design methodology the next four chapters present the necessary background for logic design the Verilog language programmable devices and computer architectures

**HDL with Digital Design** Nazeih Botros, 2015-06-25 No detailed description available for HDL with Digital Design

**A Practical Guide for Simulation and FPGA Implementation of Digital Design** Bekkay Hajji, Adel Mellit, Loubna Bouselham, 2022-03-21 This book introduces the FPGA technology used in the laboratory sessions and provides a step by step guide for designing and simulation of digital circuits It utilizes the VHDL

language which is one of the most common language used to describe the design of digital systems The Quartus II Xilinx ISE 14.7 and ModelSim software are used to process the VHDL code and make simulations and then the Altera and Xilinx FPGA platforms are employed to implement the simulated digital designs The book is composed of four parts The first part of this book has two chapters and covers various aspects FPGA architectures ASIC vs FPGA comparison FPGA design flow and basic VHDL concepts necessary to describe the design of digital systems The second part of the book includes three chapters that deal with the design of digital circuits such as combinational logic circuits sequential logic circuits and finite state machines The third part of the book is reserved for laboratory projects carried out on the FPGA platform It is a largely hands on lab class for design digital circuits and implementing their designs on the Altera FPGA platform Finally the fourth part of this work is devoted to recent applications carried out on FPGAs in particular advanced techniques in renewable energy systems The book is primarily intended for students scholars and industrial practitioners interested in the design of modern digital systems

**Exploring Computer Organization and Architecture** Tarun Varma, Laxminarayan Gahalod, Shraddha Shrivastava, 2024-05-09



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