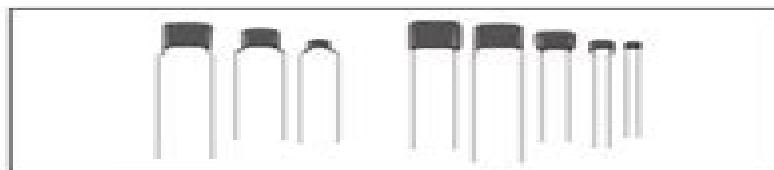


Ceramic Capacitors

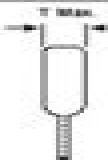
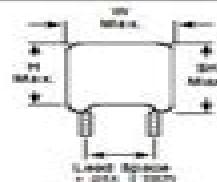
Monolithic®, Conformal-Coated, Radial Lead



FEATURES

- EIA characteristics C0G, X7R, Z5U
- Capacitance range 10pF to 4.7nF
- Voltage ratings 50 WVDC to 100 WVDC
- Wide choice of lead spacing

DIMENSIONAL CONFIGURATIONS (Numbers in brackets indicate multi-dimensional)



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TYPE	WV (MM)	H (MM)	T (MM)	LEAD SIZE		LEAD SPACING (S)					
				ARVO NO.	ODA	.10 (.25-34)	.20 (.50-68)	.25 (.63-75)	.30 (.75-82)	.37 (.94-96)	.40 (.99-100)
SC	130 (3,300)	120 (3,048)	100 (2,540)	26	.056 (1.406)	X	X	X	X	X	X
SC	200 (5,040)	250 (6,380)	125 (3,175)	24	.050 (1.300)	X	X	X	X	X	X
SC	300 (7,620)	350 (7,420)	157 (3,940)	24	.050 (1.300)	X	X	X	X	X	X
SC	400 (10,160)	400 (10,160)	157 (3,940)	24	.050 (1.300)	X	X	X	X	X	X
SC	500 (12,700)	500 (12,700)	200 (5,080)	24	.050 (1.300)	X	X	X	X	X	X

HOW TO USE IT

IC TYPE	16 LEAD SPACING CODE	23U EIA TEMPERATURE CHARACTERISTIC	164 CAPACITANCE CODE	16 CAPACITANCE TOLERANCE	630 DC VOLTAGE RATING	8 LEAD LENGTH*														
	See Dimensional Configurations.		This is expressed in decifarce. The first two digits are the significant figures. The third is the number of zeroes to follow.		This is expressed in volts. To complete the three-digit block, zeroes precede the voltage rating.															
	<table border="1"> <thead> <tr> <th>CODE</th><th>TEMPERATURE RANGE</th><th>MAX. CAP. CHANGE OVER TEMP. RANGE</th></tr> </thead> <tbody> <tr> <td>23U</td><td>+70°C to +85°C</td><td>-56%</td></tr> <tr> <td>XTR</td><td>-55°C to +125°C</td><td>+22%</td></tr> <tr> <td>CSD</td><td>-55°C to +125°C</td><td>+15%</td></tr> <tr> <td></td><td></td><td>0 = 30PPM/°C</td></tr> </tbody> </table>	CODE	TEMPERATURE RANGE	MAX. CAP. CHANGE OVER TEMP. RANGE	23U	+70°C to +85°C	-56%	XTR	-55°C to +125°C	+22%	CSD	-55°C to +125°C	+15%			0 = 30PPM/°C				
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EIA CODE	TOLERANCE	AVAILABLE IN EIA CHARACTERISTIC																		
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Ceramic Capacitor 1j100 Data

William F. Mullin

Ceramic Capacitor 1j100 Data:

Ceramic Capacitors, 1994 *Ceramic Capacitors*, 1991 *Data Book*, 1986 **Multilayer Ceramic Capacitors**
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Michael L DeLibero, 2016 Various industry needs such as electric vehicles and defibrillators are requiring capacitors with a high energy and power density that can operate in the presence of a DC bias in the 100s of volts Researchers have been developing technologies such as electrochemical capacitors for lower voltages and Titanium based capacitors for the higher voltage applications A critical step in this development is the characterization of the capacitors operating with a high DC bias This thesis outlines a practical method to characterize a capacitor of up to tens of microfards with a DC bias up to 500 volts A circuit has been designed to characterize a capacitor through discharge curves and a swept frequency analysis of the impedance It has a frequency range of 100Hz 100kHz a DC bias range of 0 500V and an AC signal amplitude range of 0 5V An iterative regression analysis has been developed to fit measured data to a multi term capacitor model Simulated data has been generated using Muratas 34 term model for a ceramic capacitor and fit to a six term model which incorporates dielectric absorption leakage and resistance The accuracy of the fit to the data is largely determined by the number of parameters in the capacitor model and typically has the greatest relative error near resonance The six term model fit the simulated data within 20 for the magnitude of the impedance and within 2 for the phase of the impedance The relative error outside of a single peak in the phase plot stays well below 5% Handbook of Advanced Ceramics Takaaki Tsurumi, Takuya Hoshina, 2013-04-11 Research and Development Intrinsic Reliability Subminiature Ceramic Capacitors T. I. Prokopowicz, A. R. Vaskas, SPRAGUE ELECTRIC CO NORTH ADAMS MA., 1968 The 20 000 hour voltage temperature life test matrix which involved the 0 01 microfarad C67 Case Size 1 MONOLYTHIC trade name capacitor is discussed Frequency histograms of all leakage current data and Weibull determinations of failure rates for some of the test conditions are included In addition the criteria which determined short life capacitors during testing are presented as well as a comparison of calculated assured life times with the times to actual life test failures Finally the schedule and conditions for a second voltage temperature life test matrix using 0 033 microfarad C67 device are briefly summarized **The Capacitor Handbook** Cletus J. Kaiser, 1993-03-11 An aid to alleviating the embarrassment of the many seasoned electrical engineers and electricians who can design massive and complex circuits or wiring systems but always get confused and hesitant when it comes to choosing the right or at least the best capacitor for a particular use A comprehensive look at the theory construction and applicat **The Viclan Ceramic Capacitor Handbook** Donald W. Hamer, Viclan, Inc, 1975
Characterization of Ceramic Capacitors Stephen A. Constantino, 1984 **Ceramic Capacitors**, 1991 **Multilayer**

Ceramic Capacitor Materials and Manufacture Victor Frank Capozzi,1975 **Optimizing an Environmentally Friendly Multilayered Ceramic Capacitor** Alison B. Schlobohm,2013 **ECR.** ,1979 **Leaded Ceramic Capacitors**
BC Components,1999 **Ceramic capacitors** Philips,1987

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