

# **AM335x ARM® Cortex™-A8 Microprocessors (MPUs)**

## **Technical Reference Manual**



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# Cortex A8 Technical Reference Manual

**Richard Witek**



## **Cortex A8 Technical Reference Manual:**

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Digital Equipment Corporation's new 64 bit RISC Alpha architecture Written by the designers of the internal Digital specifications this book contains complete descriptions of the common architecture required for all implementations and the interfaces required to support the OSF 1 and OpenVMS operating systems [Alpha AXP Architecture Reference Manual](#) Richard L. Sites, Richard T. Witek, 2014-05-16 Alpha AXP Architecture Reference Manual Second Edition describes the required behavior of all Alpha implementations as seen by the machine language programmer This book discusses Alpha single board computers which have been introduced to cover the high end embedded controller market Organized into five

parts this edition begins with an overview of the instruction set architecture This text then describes the supporting PALcode routines for three operating systems Other parts consider a particular console implementation that is specific to platforms that support the OpenVMS AXP or DEC OSF 1 operating systems This book discusses as well the specific operating system PALcode architecture The final part provides a discussion of console issues for Windows NT with its PALcode description This book is a valuable resource for machine language programmers     *OCOSS Version 1.00 and GTERM Version 1.00*  
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