

ModelSim[®] SE

Command Reference

Version 6.1

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A Route to Chaos Using FPGAs Bharathwaj Muthuswamy, Santo Banerjee, 2015-06-18 The purpose of this introductory book is to couple the teaching of chaotic circuit and systems theory with the use of field programmable gate arrays FPGAs. As such it differs from other texts on chaos: first it puts emphasis on combining theoretical methods, simulation tools, and physical realization to help the reader gain an intuitive understanding of the properties of chaotic systems. Second, the medium used for physical realization is the FPGA. These devices are massively parallel architectures that can be configured to realize a variety of logic functions. Hence FPGAs can be configured to emulate systems of differential equations. Nevertheless, maximizing the capabilities of an FPGA requires the user to understand the underlying hardware and also FPGA design software. This is achieved by the third distinctive feature of this book: a lab component in each chapter. Here readers are asked to experiment with computer simulations and FPGA designs to further their understanding of concepts covered in the book. This text is intended for graduate students in science and engineering interested in exploring implementation of nonlinear dynamical chaotic systems on FPGAs.

Reconfigurable Embedded Control Systems: Applications for Flexibility and Agility Khalgui, Mohamed, Hanisch, Hans-Michael, 2010-11-30 This book addresses the development of reconfigurable embedded control systems and describes various problems in this important research area which include static and dynamic manual or automatic reconfigurations, multi-agent architectures, modeling and verification, component-based approaches, architecture description languages, distributed reconfigurable architectures, real-time and low-power scheduling, execution models, and the implementation of such systems. Component Design by Example Ben Cohen, 2001

Digital VLSI Systems Design Seetharaman Ramachandran, 2007-06-14 This book deals with actual design applications rather than the technology of VLSI Systems. This book is written basically for an advanced level course in Digital VLSI Systems Design using a Hardware Design Language (HDL) Verilog. This book may be used for teaching undergraduates, graduates, and research scholars of Electrical, Electronics, Computer Science, and Engineering, Embedded Systems, Measurements, and Instrumentation, Applied Electronics, and interdisciplinary departments such as Biomedical, Mechanical, Engineering, Information Technology, Physics, etc. This book also serves as a reference design manual for practicing engineers and researchers. Although this book is written for an advanced level course, diligent freelance readers and consultants, especially those who do not have a first-level exposure of digital logic design, may also start using this book after a short-term course or self-study on digital logic design. In order to help these readers as well as regular students, the book starts with a good review of digital systems design, which lays a solid foundation to understand the rest of this book right up to involved Project Designs unfolded gradually. Contents of the Book: The book presents new source material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD tools and FPGA boards, enabling the serious readers to design VLSI Systems on their own.

Verilog Computer-Based Training Course Zainalabedin Navabi, 2002-04

Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-05-14 Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology

Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation Jorge Juan Chico, Enrico Macii, 2003-10-02 Welcome to the proceedings of PATMOS 2003 This was the 13th in a series of international workshops held in several locations in Europe Over the years PATMOS has gained recognition as one of the major European events devoted to power and timing aspects of integrated circuit and system design Despite its significant growth and development PATMOS can still be considered as a very informal forum featuring high level scientific presentations together with open discussions and panel sessions in a free and relaxed environment This year PATMOS took place in Turin Italy organized by the Politecnico di Torino with technical co sponsorship from the IEEE Circuits and Systems Society and the generous support of the European Commission as well as that of several industrial sponsors including BullDAST Cadence Mentor Graphics STMicroelectronics and Synopsys The objective of the PATMOS workshop is to provide a forum to discuss and investigate the emerging problems in methodologies and tools for the design of new generations of integrated circuits and systems A major emphasis of the technical program is on speed and low power aspects with particular regard to modeling characterization design and architectures

Future Communication Technology and Engineering Kennis Chan, 2015-04-06 This volume contains the papers presented at the 2014 International Conference on Future

Communication Technology and Engineering FCTE2014 taking place in Shenzhen China from 16 17 November 2014
Communication technologies are developing quickly and there are more possibilities for future communication technologies provided by the achievements made rather than limitations At the convention innovative and inspiring ideas were presented certain controversial topics were discussed e g what are the most efficient convenient methods for information communication and what is the most probable prospect for future communication technology It is difficult to make any definite conclusions from these presentations and discussions but the desire and drive for improvement and development shown by the participants authors are surely remarkable and respectable In this book 70 papers are included chosen from hundreds of submissions contributed by scientists from various countries and regions after careful reading and discussing by a team of reviewers These papers cover almost every possible aspect of communication technology including communication systems automation and control engineering electrical engineering AI algorithms signal processing data mining and knowledge based systems **12th International Workshop on Rapid System Prototyping** IEEE Computer Society. Design Automation Technical Committee,2001 The proceedings from the June 2001 conference in Monterey California include 30 papers on hardware case studies reconfiguring computing communications systems distributed prototyping systems modeling model based prototyping efficient evaluation methodologies and tools Keynote addresses on
Integrated Circuit and System Design ,2003 *Government Reports Announcements & Index* ,1982-08 *Safety Science Abstracts Journal* ,1983 *Government Reports Annual Index* ,1993 **EDN.** ,1983 *MSC NASTRAN for Windows* MacNeal-Schwendler Corporation (Los Angeles, Calif.),1999 *Automated Interactive Simulation Model (AISIM) User's Manual* United States. Air Force. Systems Command,J. Hearne,M. Deshler,W. Austell,Hughes Aircraft Company,United States. National Technical Information Service,1982 **ArcCAD Command Reference** ,1992 *Mentat II 2.3 Command Reference* MARC Analysis Research Corporation,1994 *Command Reference* Autodesk Inc,1995 **1400 Series Simulator Reference Manual for XDS Sigma 5/7 Computers** ,1970

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