



جامعة الأمير سطام بن عبد العزيز  
Prince Sattam Bin Abdulaziz University

# **COMPUTER HARDWARE MAINTENANCE**

## **LAB MANUAL 2015**

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# Computer Hardware Lab Manual For Diploma

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## **Computer Hardware Lab Manual For Diploma:**

Computer Hardware Laboratory Manual D. D. (Donald D.) Cowan, 1983     *A+ Guide to Hardware* Jean Andrews, Jill West (Author of CompTIA Network+ guide to networks), 2016-01-29 This lab manual contains more than 65 labs to provide additional hands on experience and to help prepare for the CompTIA A 220 901 certification exam including complete lab procedures and post lab review questions     PC Hardware and Software Lab Manual MultiPublishing Company, 2010-07-01

**Lab Manual a Hardware** Jean Andrews, 2005-04 This work prepares readers for the revised A Core Hardware Certification exam offered through the Computer Technology Industry Association CompTIA in the areas of computer hardware SCSI standards computer maintenance networking and computer memory management     **Laboratory Manual for Computer Hardware (CSC216)** , 2016     **Lab Manual for A+ Guide to Hardware** Andrews, 2003 This Lab Manual is designed to accompany the A Guide to Hardware Second Edition and provides additional hands on practice need to succeed in industry This Lab Manual is also an excellent resource to use to prepare for CompTIA s 2003 A Core Hardware certification exam     **Lab Manual for Andrews' A+ Guide to Hardware** Jean Andrews, 2013-01-01 The Lab Manual is a valuable tool designed to enhance your lab experience Lab activities objectives materials lists step by step procedures illustrations and review questions are commonly found in a Lab Manual Important Notice Media content referenced within the product description or the product text may not be available in the ebook version     *Assessment Case Studies* James Oliver Nichols, 1995     **Lab Manual for A+ Guide to Hardware** Jean Andrews, Todd Verge, 2005\*     *International Books in Print* , 1979     **Instructional Hardware Lab Manual** Linda Burton, 1995-08-01     Lab Manual for Electronic Devices, Global Edition THOMAS L. FLOYD, 2018-06-19 This laboratory manual is carefully coordinated to the text *Electronic Devices Tenth edition* Global edition by Thomas L Floyd The seventeen experiments correspond to the chapters in the text except the first experiment references Chapters 1 and the first part of Chapter 2 All of the experiments are subdivided into two or three Parts With one exception Experiment 12 B the Parts for the all experiments are completely independent of each other The instructor can assign any or all Parts of these experiments and in any order This format provides flexibility depending on the schedule laboratory time available and course objectives In addition experiments 12 through 16 provide two options for experiments These five experiments are divided into two major sections identified as A or B The A experiments continue with the format of previous experiments they are constructed with discrete components on standard protoboards as used in most electronic teaching laboratories The A experiments can be assigned in programs where traditional devices are emphasized Each B experiment has a similar format to the corresponding A experiment but uses a programmable Analog Signal Processor ASP that is controlled by free Computer Aided Design CAD software from the Anadigm company [www.anadigm.com](http://www.anadigm.com) These experiments support the Programmable Analog Design feature in the textbook The B experiments are also subdivided into independent Parts but Experiment 12 B Part 1 is a software tutorial and should be performed before any

other B experiments This is an excellent way to introduce the ASP technology because no other hardware is required other than a computer running the downloaded software In addition to Experiment 12 B the first 13 steps of Experiment 15 B Part 2 are also tutorial in nature for the AnadigmFilter program This is an amazing active filter design tool that is easy to learn and is included with the AnadigmDesigner2 AD2 CAD software The ASP is part of a Programmable Analog Module PAM circuit board from the Servenger company [www.servenger.com](http://www.servenger.com) that interfaces to a personal computer The PAM is controlled by the AD2 CAD software from the Anadigm company website Except for Experiment 12 B Part 1 it is assumed that the PAM is connected to the PC and AnadigmDesigner2 is running Experiment 16 B Part 3 also requires a spreadsheet program such as Microsoft Excel The PAM is described in detail in the Quick Start Guide Appendix B Instructors may choose to mix A and B experiments with no loss in continuity depending on course objectives and time We recommend that Experiment 12 B Part 1 be assigned if you want students to have an introduction to the ASP without requiring a hardware purchase A text feature is the Device Application DA at the end of most chapters All of the DAs have a related laboratory exercise using a similar circuit that is sometimes simplified to make laboratory time as efficient as possible The same text icon identifies the related DA exercise in the lab manual One issue is the trend of industry to smaller surface mount devices which are very difficult to work with and are not practical for most lab work For example almost all varactors are supplied as surface mount devices now In reviewing each experiment we have found components that can illustrate the device function with a traditional one The traditional through hole MV2109 varactor is listed as obsolete but will be available for the foreseeable future from Electronix Express [www.elexp.com](http://www.elexp.com) so it is called out in Experiment 3 All components are available from Electronix Express [www.elexp.com](http://www.elexp.com) as a kit of parts see list in Appendix A The format for each experiment has not changed from the last edition and is as follows Introduction A brief discussion about the experiment and comments about each of the independent Parts that follow Reading Reading assignment in the Floyd text related to the experiment Key Objectives A statement specific to each Part of the experiment of what the student should be able to do Components Needed A list components and small items required for each Part but not including the equipment found at a typical lab station Particular care has been exercised to select materials that are readily available and reusable keeping cost at a minimum Parts There are two or three independent parts to each experiment Needed tables graphs and figures are positioned close to the first referenced location to avoid confusion Step numbering starts fresh with each Part but figures and tables are numbered sequentially for the entire experiment to avoid multiple figures with the same number Conclusion At the end of each Part space is provided for a written conclusion Questions Each Part includes several questions that require the student to draw upon the laboratory work and check his or her understanding of the concepts Troubleshooting questions are frequently presented Multisim Simulation At the end of each A experiment except 1 one or more circuits are simulated in a Multisim computer simulation New Multisim troubleshooting problems have been added to this edition Multisim troubleshooting files are identified with the suffix f1 f2

etc in the file name standing for fault1 fault2 etc Other files with nf as the suffix include demonstrations or practice using instruments such as the Bode Plotter and the Spectrum Analyzer A special icon is shown with all figures that are related to the Multisim simulation Multisim files are found on the website [www.pearsonglobaledition.com](http://www.pearsonglobaledition.com) Floyd Microsoft PowerPoint slides are available at no cost to instructors for all experiments The slides reinforce the experiments with troubleshooting questions and a related problem and are available on the instructor s resource site Each laboratory station should contain a dual variable regulated power supply a function generator a multimeter and a dual channel oscilloscope A list of all required materials is given in Appendix A along with information on acquiring the PAM As mentioned components are also available as a kit from Electronix Express the kit number is 32DBEDFL10      **Lab Manual for Andrews' A+ Guide to Software** Jean Andrews,2016-01-29 This lab manual contains more than 70 labs to provide additional hands on experience and to help prepare for the CompTIA A 220 902 certification exam including complete lab procedures and post lab review questions

**Digital Hardware Laboratory Manual** Michael D. Furman,2002-01-01      **Government Reports Announcements & Index** ,1990-12      **VLSI: Integrated Systems on Silicon** Ricardo A. Reis,Luc Claesen,2013-06-05 This book contains the papers that have been presented at the ninth Very Large Scale Integrated Systems conference VLSI 97 that is organized biannually by IFIP Working Group 10 5 It took place at Hotel Serra Azul in Gramado Brazil from 26 30 August 1997 Previous conferences have taken place in Edinburgh Trondheim Vancouver Munich Grenoble and Tokyo The papers in this book report on all aspects of importance to the design of the current and future integrated systems The current trend towards the realization of versatile Systems on a Chip require attention of embedded hardware software systems dedicated ASIC hardware sensors and actuators mixed analog digital design video and image processing low power battery operation and wireless communication The papers as presented in Jhis book have been organized in two tracks where one is dealing with VLSI System Design and Applications and the other presents VLSI Design Methods and CAD The following topics are addressed VLSI System Design and Applications Track VLSI for Video and Image Processing Microsystem and Mixed mode design Communication And Memory System Design Cow voltage Low power Analog Circuits High Speed Circuit Techniques Application Specific DSP Architectures VLSI Design Methods and CAD Track Specification and Simulation at System Level Synthesis and Technology Mapping CAD Techniques for Low Power Design Physical Design Issues in Sub micron Technologies Architectural Design and Synthesis Testing in Complex Mixed Analog and Digital Systems      **Computer Lab Manual to Accompany Living with Computers** Patrick G. McKeown,1989      **A+ Guide to Managing and Maintaining Your PC** Jean Andrews,Todd Verge,2006-11 The Lab Manual for A GUIDE TO MANAGING AND MAINTAINING YOUR PC 6th Edition is a valuable tool designed to enhance your classroom experience Lab activities objectives materials lists step by step procedures illustrations review questions and more are all included      Computer Lab Manual ,1987      Digital Hardware Laboratory Manual Courier Custom Publishing, Incorporated,2002-01-01

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