

COMPUTER ORGANIZATION AND DESIGN

THE HARDWARE/SOFTWARE INTERFACE

FIFTH EDITION

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Logic Gates, Circuits, Processors, Compilers and Computers Jan Friso Groote, Rolf Morel, Julien Schmaltz, Adam Watkins, 2021-08-18 This undergraduate textbook first introduces basic electronic circuitry before explaining more advanced elements such as the Arithmetic Logic Unit sequential circuits and finally microprocessors. In keeping with this integrated and graduated approach the authors then explain the relationship to first assembly programming then higher level languages and finally computer organisation. Authors use the Raspberry Pi and ARM microprocessors for their explanations. The material has been extensively class tested at TU Eindhoven by an experienced team of lecturers and researchers. This is a modern holistic treatment of well established topics valuable for undergraduate students of computer science and electronics engineering and for self study. The authors use the Raspberry Pi and ARM microprocessors for their explanations.

Parallel Programming Thomas Rauber, Gudula Rünger, 2023-04-04 This textbook covers the new development in processor architecture and parallel hardware. It provides detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers. The book is structured in three main parts covering all areas of parallel computing: the architecture of parallel systems, parallel programming models and environments, and the implementation of efficient application algorithms. The emphasis lies on parallel programming techniques needed for different architectures. In particular, this third edition includes an extended update of the chapter on computer architecture and performance analysis, taking new developments such as the aspect of energy consumption into consideration. The description of OpenMP has been extended and now also captures the task concept of OpenMP. The chapter on message passing programming has been extended and updated to include new features of MPI such as extended reduction operations and non-blocking collective communication operations. The chapter on GPU programming also has been updated. All other chapters also have been revised carefully. The main goal of this book is to present parallel programming techniques that can be used in many situations for many application areas and to enable the reader to develop correct and efficient parallel programs. Many example programs and exercises are provided to support this goal and to show how the techniques can be applied to further applications. The book can be used as a textbook for students as well as a reference book for professionals. The material of the book has been used for courses in parallel programming at different universities for many years. Books in Print, 1994. **Computer Systems** Ata Elahi, 2022-03-16 This updated textbook covers digital design fundamentals of computer architecture and ARM assembly language. The book starts by introducing computer abstraction, basic number systems, character coding, basic knowledge in digital design and components of a computer. The book goes on to discuss information representation in computing, Boolean algebra and logic gates and sequential logic. The book also presents introduction to computer architecture, Cache mapping methods and virtual memory. The author also covers ARM architecture, ARM instructions, ARM assembly language using Keil.

development tools and bitwise control structure using C and ARM assembly language The book includes a set of laboratory experiments related to digital design using Logisim software and ARM assembly language programming using Keil development tools In addition each chapter features objectives summaries key terms review questions and problems

Optimizing HPC Applications with Intel Cluster Tools Alexander Supalov,Andrey Semin,Christopher Dahnken,Michael Klemm,2014-10-09 Optimizing HPC Applications with Intel Cluster Tools takes the reader on a tour of the fast growing area of high performance computing and the optimization of hybrid programs These programs typically combine distributed memory and shared memory programming models and use the Message Passing Interface MPI and OpenMP for multi threading to achieve the ultimate goal of high performance at low power consumption on enterprise class workstations and compute clusters The book focuses on optimization for clusters consisting of the Intel Xeon processor but the optimization methodologies also apply to the Intel Xeon Phi coprocessor and heterogeneous clusters mixing both architectures Besides the tutorial and reference content the authors address and refute many myths and misconceptions surrounding the topic The text is augmented and enriched by descriptions of real life situations *Subject Guide to Books in Print* ,1997 *Books in Series* ,1985 Vols for 1980 issued in three parts Series Authors and Titles **Books in Print**

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