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with Computer Architecture for Security provides practicing engineers and students with a clear understanding of computer hardware technologies The fundamentals of digital logic design as well as the use of the Verilog hardware description language are discussed The book covers computer organization and architecture modern design concepts and computer security through hardware Techniques for designing both small and large combinational and sequential circuits are thoroughly explained This detailed reference addresses memory technologies CPU design and techniques to increase performance microcomputer architecture including plug and play device interface and memory hierarchy A chapter on security engineering methodology as it applies to computer architecture concludes the book Sample problems design examples and detailed diagrams are provided throughout this practical resource

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Principles of Computer Hardware Alan Clements, 2006-02-09 The fourth edition of this work provides a readable tutorial based introduction to the subject of computer hardware for undergraduate computer scientists and engineers and includes a companion website to give lecturers additional notes

AN INTRODUCTION TO DIGITAL COMPUTER DESIGN, FIFTH EDITION V. RAJARAMAN, T. RADHAKRISHNAN, 2008-03-01 This highly acclaimed well established book now in its fifth edition is intended for an introductory course in digital computer design for B Sc students of computer science B Tech students of computer science and engineering and BCA MCA students of computer applications A knowledge of programming in C or Java would be useful to give the student a proper perspective to appreciate the development of the subject The first part of the book presents the basic tools and develops procedures suitable for the design of digital circuits and small digital systems It equips students with a firm understanding of logic principles before they study the intricacies of logic organization

and architecture of computers in the second part Besides discussing data representation arithmetic operations Boolean algebra and its application in designing combinatorial and sequential switching circuits the book introduces the Algorithmic State Machines which are used to develop a hardware description language for the design of digital systems The organization of a small hypothetical computer is described to illustrate how instruction sets are evolved Real computers namely Pentium and MIPS machines are described and compared with the hypothetical computer After discussing the features of a CPU I/O devices and I/O organization cache and virtual memory the book concludes with a new chapter on the use of parallelism to enhance the speed of computers Besides the fifth edition has new material in CMOS gates MSI ALU and Pentium5 architecture The chapter on Cache and Virtual Memory has been rewritten

Digital Electronics: A Primer - Introductory Logic Circuit Design Mark S Nixon, 2015-01-27 This practical introduction explains exactly how digital circuits are designed from the basic circuit to the advanced system It covers combinational logic circuits which collect logic signals to sequential logic circuits which embody time and memory to progress through sequences of states The primer also highlights digital arithmetic and the integrated circuits that implement the logic functions Based on the author's extensive experience in teaching digital electronics to undergraduates the book translates theory directly into practice and presents the essential information in a compact digestible style Worked problems and examples are accompanied by abbreviated solutions with demonstrations to ensure that the design material and the circuits operation are fully understood This is essential reading for any electronic or electrical engineering student new to digital electronics and requiring a succinct yet comprehensive introduction

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Computer Organization and Design ARM Edition David A. Patterson, John L. Hennessy, 2016-05-06 The new ARM Edition of Computer Organization and Design features a subset of the ARMv8 A architecture which is used to present the fundamentals of hardware technologies assembly language computer arithmetic

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