

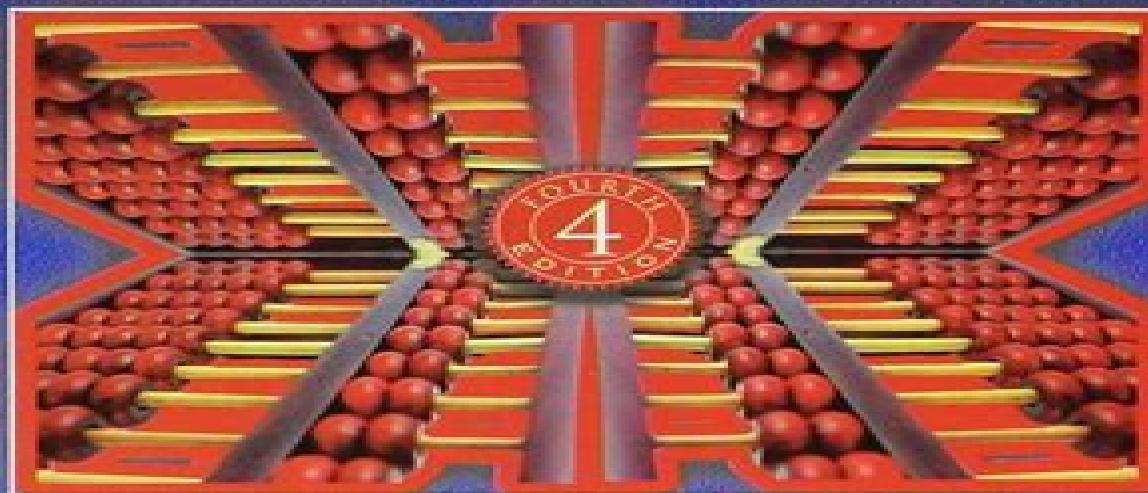
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Shiva,2007-10 **Logic Synthesis for Control Automata** Samary Baranov,2012-12-06 Logic Synthesis for Control Automata provides techniques for logic design of very complex control units with hardly any constraints on their size i e the number of inputs outputs and states These techniques cover all stages of control unit design including description of control unit behavior by using operator schemes of algorithms binary decision trees and various transformations of these descriptions composition decomposition minimization etc synthesis of a control automaton finite state machine synthesis of an automaton logic circuit with matrix structure as a part of LSI or VLSI circuits as multilevel circuit with logic gates with standard LSI and VLSI circuits with and without memory Each chapter contains many examples illustrating the use of the models and methods described Moreover the special last chapter demonstrates in detail the whole design methodology presented in the previous chapters through the examples of the logic design for a control unit The models methods and

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