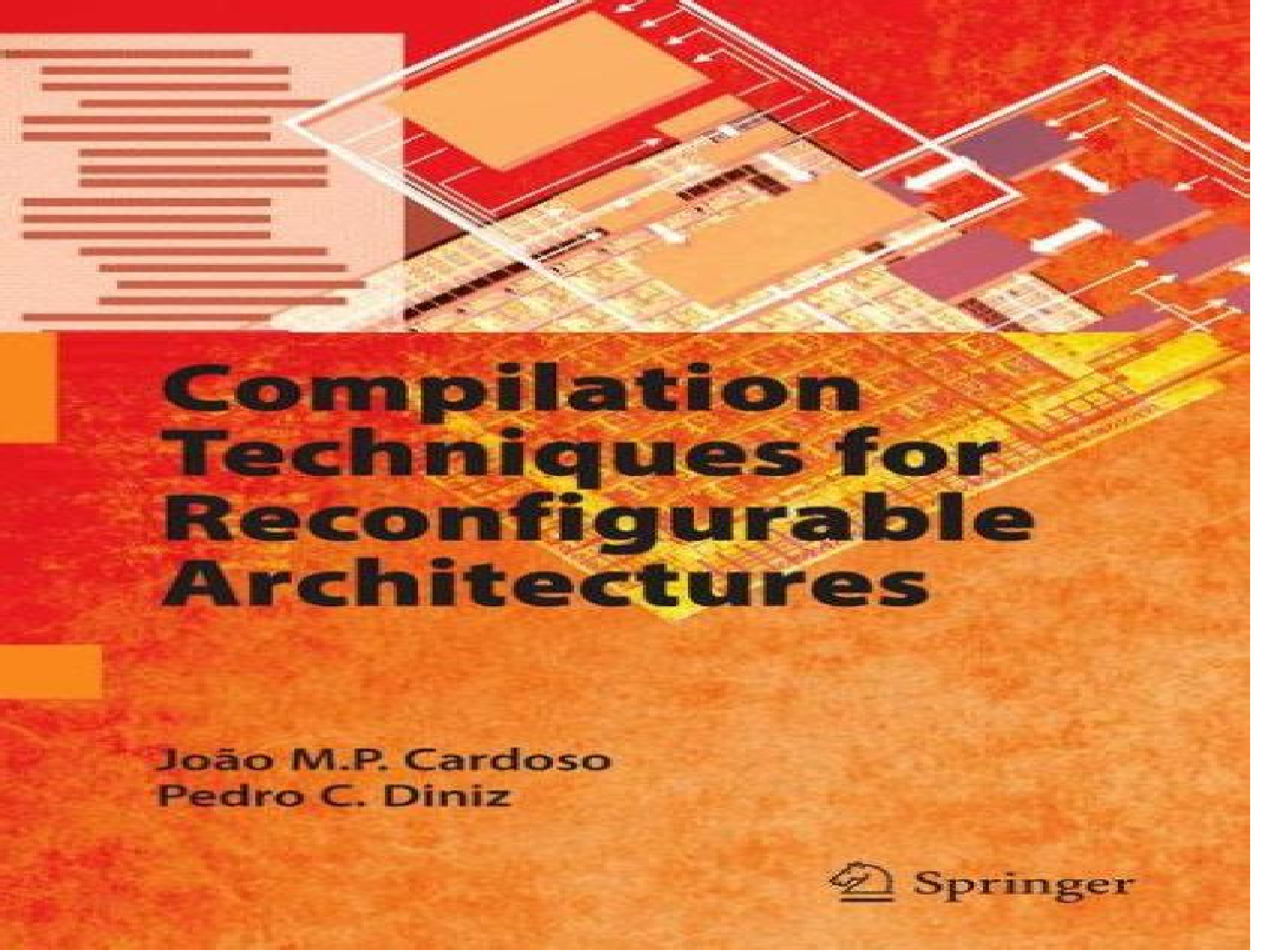


The background of the cover features a complex, abstract architectural diagram. It consists of several overlapping, semi-transparent planes in shades of red, orange, and yellow. These planes are marked with white lines and arrows, suggesting a flow or a structural layout. The overall composition is dynamic and technical, reflecting the book's focus on reconfigurable architectures.

Compilation Techniques for Reconfigurable Architectures

João M.P. Cardoso
Pedro C. Diniz

 Springer

Compilation Techniques For Reconfigurable Architectures

**Sociedade Brasileira de
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Compilation Techniques For Reconfigurable Architectures:

Compilation Techniques for Reconfigurable Architectures João M.P. Cardoso, Pedro C. Diniz, 2011-04-02 The extreme exibility of recon gurable architectures and their performance pot tial have made them a vehicle of choice in a wide range of computing domains from rapid circuit prototyping to high performance computing The increasing availab ility of transistors on a die has allowed the emergence of recon gurable architectures with a large number of computing resources and interconnection topologies To ploit the potential of these recon gurable architectures programmers are forced to map their applications typically written in high level imperative programming l guages such as C or MATLAB to hardware oriented languages such as VHDL or Verilog In this process they must assume the role of hardware designers and software programmers and navigate a maze of program transformations mapping and synthesis steps to produce ef cient recon gurable computing implementations The richness and sophistication of any of these application mapping steps make the mapping of computations to these architectures an increasingly daunting process It is thus widely believed that automatic compilation from high level programming languages is the key to the success of recon gurable computing This book describes a wide range of code transformations and mapping te niques for programs described in high level programming languages most tably imperative languages to recon gurable architectures

Computer Systems: Architectures, Modeling, and Simulation Andy Pimentel, Stamatis Vassiliadis, 2004-07-05 This book constitutes the refereed proceedings of the 4th International Workshop on Systems Architectures Modeling and Simulation SAMOS 2004 held in Samos Greece on July 2004 Besides the SAMOS 2004 proceedings the book also presents 19 revised papers from the predecessor workshop SAMOS 2003 The 55 revised full papers presented were carefully reviewed and selected for inclusion in the book The papers are organized in topical sections on reconfigurable computing architectures and implementation and systems modeling and simulation

Handbook of Computer Architecture Anupam Chattopadhyay, 2024-12-20 This handbook presents the key topics in the area of computer architecture covering from the basic to the most advanced topics including software and hardware design methodologies It will provide readers with the most comprehensive updated reference information covering applications in single core processors multicore processors application specific processors reconfigurable architectures emerging computing architectures processor design and programming flows test and verification This information benefits the readers as a full and quick technical reference with a high level review of computer architecture technology detailed technical descriptions and the latest practical applications

Software Defined Chips Shaojun Wei, Leibo Liu, Jianfeng Zhu, Chenchen Deng, 2022-10-20 This is the first book of a two volume book set which introduces software defined chips In this book it introduces the conceptual evolution of software defined chips from the development of integrated circuits and computing architectures Technical principles characteristics and key issues of software defined chips are systematically analyzed The hardware architecture design methods are described involving architecture design primitives hardware design

spaces and agile design methods From the perspective of the compilation system the complete process from high level language to configuration contexts is introduced in detail This book is suitable for scientists and researchers in the areas of electrical and electronic engineering and computer science Postgraduate students practitioners and professionals in related areas are also potentially interested in the topic of this book Field Programmable Logic and Applications Peter Y.K. Cheung, Georg A. Constantinides, Jose T. de Sousa, 2003-10-02 This book contains the papers presented at the 13th International Workshop on Field Programmable Logic and Applications FPL held on September 1 3 2003 The conference was hosted by the Institute for Systems and Computer Engineering Research and Development of Lisbon INESC ID and the Department of Electrical and Computer Engineering of the IST Technical University of Lisbon Portugal The FPL series of conferences was founded in 1991 at Oxford University UK and has been held annually since in Oxford 3 times Vienna Prague Darmstadt London Tallinn Glasgow Villach Belfast and Montpellier It brings together academic researchers industrial experts users and newcomers in an informal welcoming atmosphere that encourages productive exchange of ideas and knowledge between delegates Exciting advances in field programmable logic show no sign of slowing down New grounds have been broken in architectures design techniques run time configuration and applications of field programmable devices in several different areas Many of these innovations are reported in this volume The size of FPL conferences has grown significantly over the years FPL in 2002 saw 214 papers submitted representing an increase of 83% when compared to the year before The interest and support for FPL in the programmable logic community continued this year with 216 papers submitted The technical program was assembled from 90 selected regular papers and 56 posters resulting in this volume of proceedings The program also included three invited plenary keynote presentations from LSI Logic Xilinx and Cadence and three industrial tutorials from Altera Mentor Graphics and Daffa *Reconfigurable Technology*, 2000 **SBCCI 2007** Antonio Petraglia, Association for Computing Machinery, 2007 XII Symposium on Integrated Circuits and Systems Design Sociedade Brasileira de Computação, IFIP WG 10.5, 1999 Topics in these conference papers include microprocessors design modelling co design analog design high level synthesis digital design synthesis and reconfiguration CAD tools and IP cores VLSI Circuits and Systems, 2005 **Parallel and Distributed Processing**, 1999 **Proceedings of the ... International Symposium on Hardware/Software Codesign**, 2002 **Computer Architecture**, 1999 **Automatic Compilation of C for Hybrid Reconfigurable Architectures** Timothy John Callahan, 2002 **Computer Architecture'99** John Morris, 1999-04 This volume contains the proceedings of the 4th Australasian Conference on Computer Architecture ACAC9 held in Auckland New Zealand in January 1999 Topics at this conference included areas on computer architecture parallel and superscalar processors computer interconnection and computer methods This book of 21 selected contributed papers therefore presents a collection of new and innovative ideas in computer architecture addressing all components of a high performance system Proceedings, 2007 **Second International Workshop on Digital and Computational Video** Vijay K. Jain, 2001

IEEE Computer Society Order Number PR001110 verso of T p *Reduced Complexity VLSI Implementation of Discrete*
Wavelet Transform for Image and Video Compression Philip Phuc Dang,2002 **FPGA ...** ,2007 *SBCCI 2006* ,2006
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