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detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers The book is structured in three main parts covering all areas of parallel computing the architecture of parallel systems parallel programming models and environments and the implementation of efficient application algorithms The emphasis lies on parallel programming techniques needed for different architectures In particular this third edition includes an extended update of the chapter on computer architecture and performance analysis taking new developments such as the aspect of energy consumption into consideration The description of OpenMP has been extended and now also captures the task concept of OpenMP The chapter on message passing programming has been extended and updated to include new features of MPI such as extended reduction operations and non blocking collective communication operations The chapter on GPU programming also has been updated All other chapters also have been revised carefully The main goal of this book is to present parallel programming techniques that can be used in many situations for many application areas and to enable the reader to develop correct and efficient parallel programs Many example programs and exercises are provided to support this goal and to show how the techniques can be applied to further applications The book can be used as a textbook for students as well as a reference book for professionals The material of the book has been used for courses in parallel programming at different universities for many years

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principles ranging from the commonly recognized to the groundbreaking that are key to breaking these bottlenecks The rest of the book is devoted to a systematic application of these principles to bottlenecks found specifically in endnodes interconnect devices and specialty functions such as security and measurement that can be located anywhere along the network This immensely practical clearly presented information will benefit anyone involved with network implementation as well as students who have made this work their goal FOR INSTRUCTORS To obtain access to the solutions manual for this title simply register on our textbook website textbooks.elsevier.com and request access to the Computer Science subject area Once approved usually within one business day you will be able to access all of the instructor only materials through the Instructor Manual link on this book's academic web page at textbooks.elsevier.com Addresses the bottlenecks found in all kinds of network devices data copying control transfer demultiplexing timers and more and offers ways to break them Presents techniques suitable specifically for endnodes including Web servers Presents techniques suitable specifically for interconnect devices including routers bridges and gateways Written as a practical guide for implementers but full of valuable insights for students teachers and researchers Includes end of chapter summaries and exercises

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