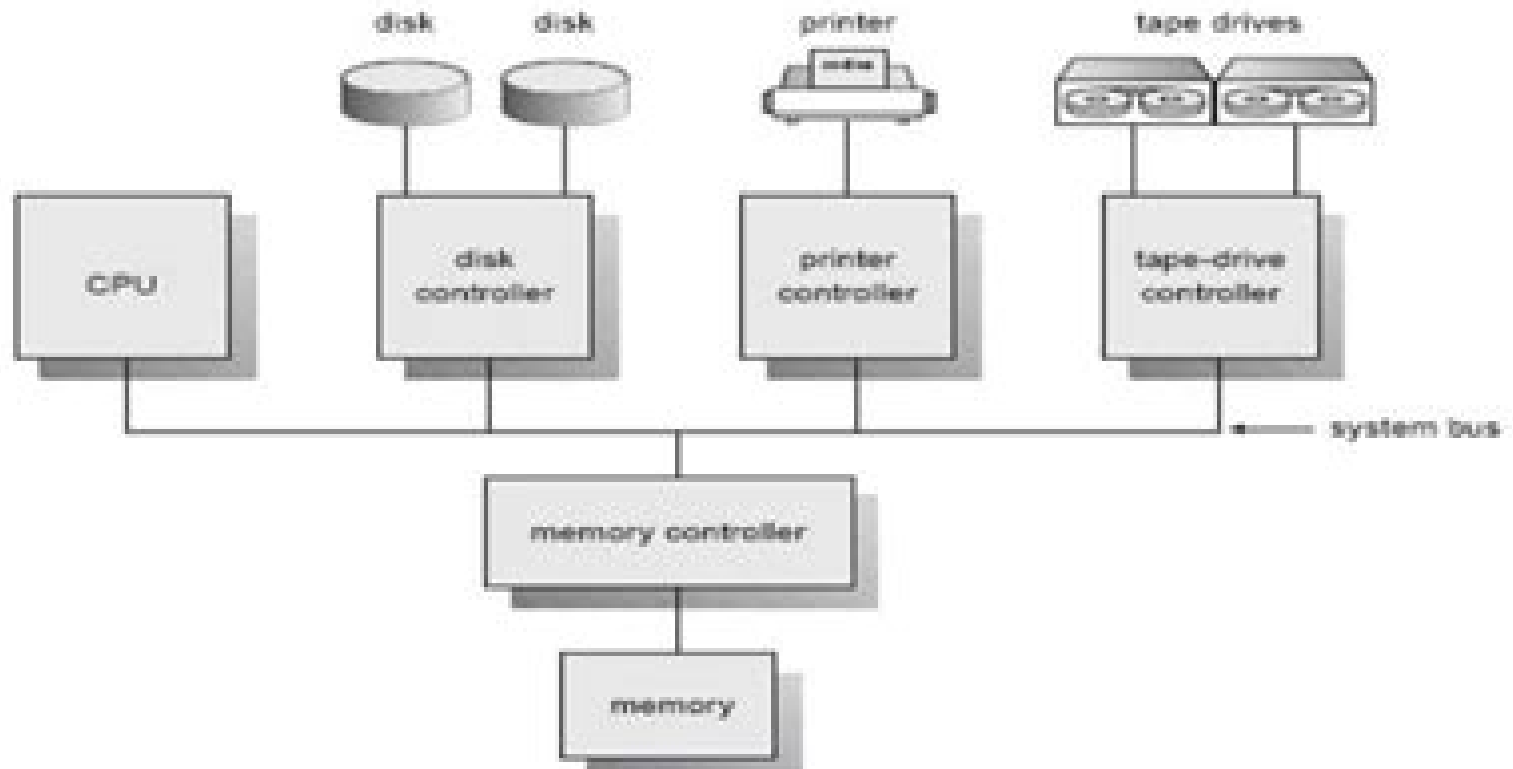


# Computer-System Architecture



# Computer System Architecture Jacob

**Lei Shi**



## **Computer System Architecture Jacob:**

**The Memory System** Bruce Jacob, 2009-07-08 Today computer system optimization at both the hardware and software levels must consider the details of the memory system in its analysis failing to do so yields systems that are increasingly inefficient as those systems become more complex This lecture seeks to introduce the reader to the most important details of the memory system it targets both computer scientists and computer engineers in industry and in academia Roughly speaking computer scientists are the users of the memory system and computer engineers are the designers of the memory system Both can benefit tremendously from a basic understanding of how the memory system really works the computer scientist will be better equipped to create algorithms that perform well and the computer engineer will be better equipped to design systems that approach the optimal given the resource limitations Currently there is consensus among architecture researchers that the memory system is the bottleneck and this consensus has held for over a decade Somewhat inexplicably most of the research in the field is still directed toward improving the CPU to better tolerate a slow memory system as opposed to addressing the weaknesses of the memory system directly This lecture should get the bulk of the computer science and computer engineering population up the steep part of the learning curve Not every CS CE researcher developer needs to do work in the memory system but just as a carpenter can do his job more efficiently if he knows a little of architecture and an architect can do his job more efficiently if he knows a little of carpentry giving the CS CE worlds better intuition about the memory system should help them build better systems both software and hardware Table of Contents Primers It Must Be Modeled Accurately and It Will Change Soon

**The Memory System** Bruce Jacob, 2009-06-19 Today computer system optimization at both the hardware and software levels must consider the details of the memory system in its analysis failing to do so yields systems that are increasingly inefficient as those systems become more complex This lecture seeks to introduce the reader to the most important details of the memory system it targets both computer scientists and computer engineers in industry and in academia Roughly speaking computer scientists are the users of the memory system and computer engineers are the designers of the memory system Both can benefit tremendously from a basic understanding of how the memory system really works the computer scientist will be better equipped to create algorithms that perform well and the computer engineer will be better equipped to design systems that approach the optimal given the resource limitations Currently there is consensus among architecture researchers that the memory system is the bottleneck and this consensus has held for over a decade Somewhat inexplicably most of the research in the field is still directed toward improving the CPU to better tolerate a slow memory system as opposed to addressing the weaknesses of the memory system directly This lecture should get the bulk of the computer science and computer engineering population up the steep part of the learning curve Not every CS CE researcher developer needs to do work in the memory system but just as a carpenter can do his job more efficiently if he knows a little of architecture and an architect can do his job more efficiently if he knows a

little of carpentry giving the CS CE worlds better intuition about the memory system should help them build better systems both software and hardware

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It Must Be Modeled Accurately and It Will Change Soon Cache and Memory Hierarchy Design Steven A. Przybylski,1990 A widely read and authoritative book for hardware and software designers This innovative book exposes the characteristics of performance optimal single and multi level cache hierarchies by approaching the cache design process through the novel perspective of minimizing execution time *ECPPM 2022 - eWork and eBusiness in Architecture, Engineering and Construction 2022* Eilif Hjelseth,Sujesh F. Sujan,Raimar J Scherer,2023-03-29

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*Operating System Concepts* Abraham Silberschatz, Peter B. Galvin, Greg Gagne, 2005 This new seventh edition of the book has been brought up to date to include recent developments in operating systems such as Windows XP and the new small footprint operating systems that work in hand held devices such as the Palm and in cell phones Most of the book is on general purpose operating systems such as Linux and those from Microsoft But at the end of the book there are chapters on other types of operating such as Real Time Operating Systems and MultiMedia OS's Finally there are some chapters which the authors call case studies In these one chapter goes into a detailed discussion of Linux another chapter covers Windows XP Chapter 23 covers several early operating systems that helped to define the features that make up modern OS's These include Atlas XDS 940 THE RC 4000 CTSS MULTICS OS 360 and MACH along with brief mentions of several others Note that this not a book on how to use operating systems this is a book on how operating systems are designed It is intended for upper level undergraduate students or first year graduate students **Scientific and Technical Aerospace Reports**, 1984

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