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Designing for Performance

Eight Edition

William Stallings

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Handbook of Networked and Embedded Control Systems Dimitrios

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Essentials of computing systems - 2^a edição João M.

Fernandes, 2022-06-21 Computers were originally invented to solve all sort of mathematical problems Nowadays computers do much more than that and are present in all human activities In fact a computer is a fantastic machine capable of doing the most amazing tasks if an appropriate program is provided A computer system contains hardware and system software that work together to run software applications Interestingly the underlying concepts that support the construction of a computer are relatively stable In fact almost all computer systems have a similar organisation i e their hardware and software components are arranged in hierarchical layers and perform similar functions This book was written for programmers and software engineers who want to comprehend how the components of a computer work and how they affect the correctness and performance of their programs

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Performance, 6/e , Computer Organization and Architecture Stallings, 2008-02 Computer Organization and Design

MIPS Edition David A. Patterson, John L. Hennessy, 2020-11-24 Computer Organization and Design The Hardware Software Interface Sixth Edition the leading award winning textbook from Patterson and Hennessy used by more than 40 000 students per year continues to present the most comprehensive and readable introduction to this core computer science topic Improvements to this new release include new sections in each chapter on Domain Specific Architectures DSA and updates

on all real world examples that keep it fresh and relevant for a new generation of students Covers parallelism in depth with examples and content highlighting parallel hardware and software topics Includes new sections in each chapter on Domain Specific Architectures DSA Discusses and highlights the Eight Great Ideas of computer architecture including Performance via Parallelism Performance via Pipelining Performance via Prediction Design for Moore's Law Hierarchy of Memories Abstraction to Simplify Design Make the Common Case Fast and Dependability via Redundancy

Micropocessors and Interfacing Techniques

Swapneel Chandrakant Mhatre, 2014-01-04 The book is written as per the syllabus of the subject Microprocessors and Interfacing Techniques for S E Computer Engineering Semester II of University of Pune It focuses on the three main parts in the study of microprocessors the architecture the programming and the system design The 8086 microprocessor is described in detail along with glimpses of 8088 80186 and 80188 microprocessors The various peripheral controllers for 8086 88 are also discussed Other topics that are related to the syllabus but not explicitly mentioned are included in the appendices Key Features Programs are given and the related theory is discussed within the same section thereby maintaining a smooth flow and also eliminating the need for a separate section on the practical experiments for the subject of Microprocessors and Interfacing Laboratory Both DOS based programs as well as kit programs are given Algorithms and flowcharts are given before DOS based programs for easy understanding of the program logic

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William Stallings, 2015-01-16 This is the eBook of the printed book and may not include any media website access codes or print supplements that may come packaged with the bound book For graduate and undergraduate courses in computer science computer engineering and electrical engineering Fundamentals of Processor and Computer Design Computer Organization and Architecture is a comprehensive coverage of the entire field of computer design updated with the most recent research and innovations in computer structure and function With clear concise and easy to read material the Tenth Edition is a user friendly source for people studying computers Subjects such as I/O functions and structures RISC and parallel processors are explored integratively throughout with real world examples enhancing the text for reader interest With brand new material and strengthened pedagogy this text engages readers in the world of computer organization and architecture

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