

Lab No. 01

INTRODUCTION TO DIGITAL LOGIC DESIGN LAB EQUIPMENT

Objective:

To Study the operation of different parts of Digital Analog Training System (ETS-7000)

Tools:

1- Digital Analog Trainer (ETS-7000):

The digital analog trainer is designed for beginners to enhance their concepts about digital and analog theory.

It has a solderless breadboard with other parts like switches, LEDs, Seven Segment Display and other slots for wires.

2- Breadboard:

It is used for the prototyping and hardware simulation of a circuit. It is used to test and check a circuit before soldering the circuit components on a PCB or a Vero board. It makes the testing and analysis of a circuit, a reversible and easy to troubleshoot process unlike the analysis of a circuit on a Vero board.

3- Jumper Wire:

This wire is used with a breadboard to transmit electric signals. It is used as a conductor with high conductivity and low resistivity.

4- Logic Indicator:

This indicator is mounted on the trainer and is used to monitor the logic state of the circuit.

5- Logic Switch:

It is a diode arrangement present on the ETS-7000 trainer and is capable of directing an input signal.

6- DC Power Supply:

It is an electronic device used in order to supply DC energy to an electrical load.



Figure 1: ETS-7000 (Digital Analog Trainer)



Figure 2: Jumper Wires



Digital Logic Design Lab Manual

Rose Arny

Digital Logic Design Lab Manual:

Digital Logic Design, Lab Manual, Volume-I Asadullah Khan,2017-03-24 [Introduction to Logic Circuits & Logic Design with Verilog](#) Brock J. LaMeres,2017-04-17 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers Coverage includes both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based Using this textbook enables readers to design digital systems using the modern HDL approach but they have a broad foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the presentation with learning Goals and assessment at its core Each section addresses a specific learning outcome that the student should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome [Lab Manual](#)

Gregory L. Moss,1991 **Digital Logic Design** John F. Passafiume,Michael Douglas,1985 [Introduction to Logic Circuits & Logic Design with VHDL](#) Brock J. LaMeres,2023-10-24 This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the content with learning goals and assessment at its core Each section addresses a specific learning outcome that the learner should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome This book can be used for either a sequence of two courses consisting of an introduction to logic circuits Chapters 1 7 followed by logic design Chapters 8 14 or a single accelerated course that uses the early chapters as reference material [Logic Design Projects Using Standard Integrated Circuits](#) John F. Wakerly,1976 [Introduction to Digital Logic](#) Textbook

Equity,2014-09-10 This textbook released under a Creative Commons Share Alike CC BY SA license is presented in its original format with the adademic content unchanged It was authored by James Feher and reviewed by colleagues and provided by the University of Georgia s Global Textbook Project This lab manual provides an introduction to digital logic starting with simple gates and building up to state machines Students should have a solid understanding of algebra as well as a rudimentary understanding of basic electricity including voltage current resistance capacitance inductance and how they

relate to direct current circuits **Design of Digital Systems** Gregory L. Moss,Moss,1997-06 **Digital Applications for CPLDs** Robert K. Dueck,Leo Chartrand,1999-06-08 Until now digital logic or digital design courses have primarily focused on using fixed function TTL and CMOS integrated circuits as the vehicle for teaching principles of logic design However the digital design field has turned a corner more and more digital designs are being implemented in Programmable Logic Devices PLDs This unique lab manual addresses this new trend by focusing on PLDs as a vehicle for teaching the new digital paradigm Digital Logic I.C. Lab Manual Gary E. Jones,1978* Forthcoming Books Rose Arny,2002

Microelectronics Education Adrian M. Ionescu,Michel Declercq,Maher Kayal,Yusuf Leblebici,2013-03-19 In this book key contributions on developments and challenges in research and education on microelectronics microsystems and related areas are published Topics of interest include but are not limited to emerging fields in design and technology new concepts in teaching multimedia in microelectronics industrial roadmaps and microelectronic education curricula nanoelectronics teaching long distance education The book is intended for academic education level and targets professors researchers and PhDs involved in microelectronics and or more generally in electrical engineering microsystems and material sciences The 2004 edition of European Workshop on Microelectronics Education EWME is particularly focused on the interface between microelectronics and bio medical sciences **Digital Logic Simulation with CPLD Programming** Steve Waterman,2003 This lab manual written around software and hardware developments of the past ten years focuses on the fundamentals of digital electronics and use of Max Plus II software by Altera Corporation Lab sequences start with digital gates and logic control circuits progress to MSI devices latches and flip flops and cover clock dependent circuits and the LPM_MACRO functions available in the software For individuals preparing to be computer technicians Fundamentals of Digital Electronics Robert K. Dueck,1994 **Digital Design** Thomas Collins,Christopher Twigg,2009 **Digital Logic Discrete Lab Manual** Gary E. Jones,1979* Digital Logic Simulation and CPLD Programming with VHDL Steve Waterman,2003 For

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