

# COMPUTER-aided LOGICAL DESIGN WITH EMPHASIS ON TEST

By John R. Haug, Jr.



# Computer Aided Logical Design With Emphasis On Vlsi

## 4th Edition

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**Computer Aided Logical Design with Emphasis on VLSI** Frederick J. Hill, Gerald R. Peterson, 1993-02-11 Tied to no particular set of computer aided logic design tools it advocates the new emphasis in VLSI design. Includes support of layout synthesis from description in a register transfer level language as well as from design capture. Contains a detailed introduction to Boolean algebra, Karnaugh maps and sequential circuits. In this edition discussion of combination logic has been extended, switching circuits updated, a comprehensive treatment of test generation for VLSI included. [EBOOK:](#)

**Fundamentals of Digital Logic** Stephen Brown, Zvonko Vranesic, 2008-07-16 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits. The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism. It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips. Fundamental concepts are illustrated by using small examples which are easy to understand. Then a modular approach is used to show how larger circuits are designed. VHDL is a complex language so it is introduced gradually in the book. Each VHDL feature is presented as it becomes pertinent for the circuits being discussed. While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools. A CD ROM containing all of the VHDL design examples used in the book as well as Altera's Quartus II CAD software is included free with every text.

**Digital Logic Testing and Simulation** Alexander Miczo, 2003-10-10 Your road map for meeting today's digital testing challenges. Today digital logic devices are common in products that impact public safety including applications in transportation and human implants. Accurate testing has become more critical to reliability, safety and the bottom line. Yet as digital systems become more ubiquitous and complex the challenge of testing them has become more difficult. As one development group designing a RISC stated the work required to test a chip of this size approached the amount of effort required to design it. A valued reference for nearly two decades, Digital Logic Testing and Simulation has been significantly revised and updated for designers and test engineers who must meet this challenge. There is no single solution to the testing problem. Organized in an easy to follow sequential format this Second Edition familiarizes the reader with the many different strategies for testing and their applications and assesses the strengths and weaknesses of the various approaches. The book reviews the building blocks of a successful testing strategy and guides the reader on choosing the best solution for a particular application. Digital Logic Testing and Simulation Second Edition covers such key topics as Binary Decision Diagrams (BDDs) and cycle based simulation. Tester architectures, Standard Test Interface Language (STIL), Practical algorithms written in a Hardware Design Language (HDL), Fault tolerance, Behavioral Automatic Test Pattern Generation (ATPG), The development of the Test Design Expert (TDX), the many obstacles encountered and lessons learned in creating this novel testing approach. Up to date and comprehensive, Digital Logic Testing and Simulation is an important resource for anyone charged with pinpointing faulty products and assuring quality, safety and reliability.

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Friedman, 2008-11-16 History of the Book The last three decades have witnessed an explosive development in integrated circuit fabrication technologies The complexities of current CMOS circuits are reaching beyond the 65 nanometer feature size and multi hundred million transistors per integrated circuit To fully exploit this technological potential circuit designers use sophisticated Computer Aided Design CAD tools While supporting the talents of innumerable microelectronics engineers these CAD tools have become the enabling factor responsible for the successful design and implementation of thousands of high performance large scale integrated circuits This book a research monograph originated from a body of doctoral dissertation research completed by the first author at the University of Rochester from 1994 to 1999 while under the supervision of Prof Eby G Friedman This research focuses on issues in the design of the clock distribution network in large scale high performance digital synchronous circuits and particularly on algorithms for non zero clock skew scheduling During the development of this research it became clear that incorporating timing issues into the successful integrated circuit design process is of fundamental importance particularly in that advanced theoretical developments in this area have been

slow to reach the designers desktops The second edition of the book is enhanced by the body of doctoral dissertation research completed by the second author at the University of Pittsburgh from 2000 to 2005 under the supervision of Prof

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Rex, 2002-12-13 Over 130 years ago James Clerk Maxwell introduced his hypothetical demon as a challenge to the scope of the second law of thermodynamics Fascination with the demon persisted throughout the development of statistical and quantum physics information theory and computer science and links have been established between Maxwell's demon and each of *Evaluation of Computer Aided Software as a Space Analysis Tool for Outfit Unit Design and Planning* Patrick D. Cahill, 1995

**Computer Organization** V. Carl Hamacher, Zvonko G. Vranesic, Safwat G. Zaky, 2002 This book provides comprehensive coverage of computer organization It presents hardware design principles and show how hardware design is influenced by the requirements of software **Encyclopedia of Library and Information Science** Allen Kent, Harold Lancour, William Z. Nasri, Jay Elwood Daily, 1968 Vol 73 index to v 48 72

**International Journal of Electrical Engineering Education**, 1995 The Cumulative Book Index, 1994 A world list of books in the English language

**Field-Programmable Gate Arrays** John V. Oldfield, Richard C. Dorf, 1995-01-23 Due to unique advantages like security improved testing and reprogrammability field programmable gate arrays are making broad inroads in the electronics industry This comprehensive overview of the topic explains the underlying principles strengths and limitations of a range of FPGA architectures Includes abundant references and illustrations Proceedings Euromicro. Conference, 1998 **IBM Systems Journal** International Business Machines Corporation, 1996 **Subject Guide to Books in Print**, 1996

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