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Alan Clements

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Computer Organization and Design David A. Patterson, John L. Hennessy, 2004-08-07 This best selling text on computer organization has been thoroughly updated to reflect the newest technologies Examples highlight the latest processor designs benchmarking standards languages and tools As with previous editions a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system The book presents an entire MIPS instruction set instruction by instruction the fundamentals of assembly language computer arithmetic pipelining memory hierarchies and I/O A new aspect of the third edition is the explicit connection between program performance and CPU performance The authors show how hardware and software components such as the

specific algorithm programming language compiler ISA and processor implementation impact program performance Throughout the book a new feature focusing on program performance describes how to search for bottlenecks and improve performance in various parts of the system The book digs deeper into the hardware software interface presenting a complete view of the function of the programming language and compiler crucial for understanding computer organization A CD provides a toolkit of simulators and compilers along with tutorials for using them For instructor resources click on the grey companion site button found on the right side of this page This new edition represents a major revision New to this edition Entire Text has been updated to reflect new technology 70% new exercises Includes a CD loaded with software projects and exercises to support courses using a number of tools A new interior design presents defined terms in the margin for quick reference A new feature Understanding Program Performance focuses on performance from the programmer's perspective Two sets of exercises and solutions For More Practice and In More Depth are included on the CD Check Yourself questions help students check their understanding of major concepts Computers In the Real World feature illustrates the diversity of uses for information technology More detail below **The Datacenter as a Computer** Luiz André Barroso, Urs Hölzle, Parthasarathy Ranganathan, 2018-10-29 This book describes warehouse scale computers WSCs the computing platforms that power cloud computing and all the great web services we use every day It discusses how these new systems treat the datacenter itself as one massive computer designed at warehouse scale with hardware and software working in concert to deliver good levels of internet service performance The book details the architecture of WSCs and covers the main factors influencing their design operation and cost structure and the characteristics of their software base Each chapter contains multiple real world examples including detailed case studies and previously unpublished details of the infrastructure used to power Google's online services Targeted at the architects and programmers of today's WSCs this book provides a great foundation for those looking to innovate in this fascinating and important area but the material will also be broadly interesting to those who just want to understand the infrastructure powering the internet The third edition reflects four years of advancements since the previous edition and nearly doubles the number of pictures and figures New topics range from additional workloads like video streaming machine learning and public cloud to specialized silicon accelerators storage and network building blocks and a revised discussion of data center power and cooling and uptime Further discussions of emerging trends and opportunities ensure that this revised edition will remain an essential resource for educators and professionals working on the next generation of WSCs

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