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William Stallings



Computer Organization And Architecture Global Edition

RAJARAMAN, V., RADHAKRISHNAN, T

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Computer Organization and Architecture Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book provides comprehensive coverage of the fundamental concepts of computer organization and architecture. Its focus on real world examples encourages students to understand how to apply essential organization and architecture concepts in the computing world. The book teaches you both the hardware and software aspects of the computer. It explains computer components and their functions, interconnection structures, bus

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COMPUTER ORGANIZATION AND ARCHITECTURE RAJARAMAN, V., RADHAKRISHNAN, T, 2007-06-01 Designed as an introductory text for the students of computer science computer applications electronics engineering and information technology for their first course on the organization and architecture of computers this accessible student friendly text gives a clear and in depth analysis of the basic principles underlying the subject This self contained text devotes one full chapter to the basics of digital logic While the initial chapters describe in detail about computer organization including CPU design ALU design memory design and I O organization the text also deals with Assembly Language Programming for Pentium using NASM assembler What distinguishes the text is the special attention it pays to Cache and Virtual Memory organization as well as to RISC architecture and the intricacies of pipelining All these discussions are climaxed by an illuminating discussion on parallel computers which shows how processors are interconnected to create a variety of parallel computers **KEY FEATURES** Self contained presentation starting with data representation and ending with advanced parallel computer

architecture Systematic and logical organization of topics Large number of worked out examples and exercises Contains basics of assembly language programming Each chapter has learning objectives and a detailed summary to help students to quickly revise the material *Structured Computer Organization* Andrew S. Tanenbaum,Todd Austin,2013 Structured Computer Organization specifically written for undergraduate students provides an accessible introduction to computer hardware and architecture This text also serves as a useful resource for all computer professionals and engineers who need an overview or introduction to computer architecture **Computer Organization and Architecture** Stallings,2008-02

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Computer Organization and Design David A. Patterson,John L. Hennessy,2004-08-07 This best selling text on computer organization has been thoroughly updated to reflect the newest technologies Examples highlight the latest processor designs benchmarking standards languages and tools As with previous editions a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system The book presents an entire MIPS instruction set instruction by instruction the fundamentals of assembly language computer arithmetic pipelining memory hierarchies and I/O A new aspect of the third edition is the explicit connection between program performance and CPU performance The authors show how hardware and software components such as the specific algorithm programming language compiler ISA and processor implementation impact program performance Throughout the book a new feature focusing on program performance describes how to search for bottlenecks and improve performance in various parts of the system The book digs deeper into the hardware software interface presenting a complete view of the function of the programming language and compiler crucial for understanding computer organization A CD provides a toolkit of simulators and compilers along with tutorials for using them For instructor resources click on the grey companion site button found on the right side of this page This new edition represents a major revision New to this edition Entire Text has been updated to reflect new technology 70% new exercises Includes a CD loaded with software projects and exercises to support courses using a number of tools A new interior design presents defined terms in the margin for quick reference A new feature Understanding Program Performance focuses on performance from the programmer s perspective Two sets of exercises and solutions For More Practice and In More Depth are included on the CD Check Yourself questions help students check their understanding of major concepts Computers In the Real World feature illustrates the diversity of uses for information technology More detail below

Computer Organization and Architecture William Stallings,2018 **Intelligent Sustainable Systems** Atulya K. Nagar,Dharm Singh Jat,Durgesh Kumar Mishra,Amit Joshi,2024-04-08 This book provides insights of World Conference on Smart Trends in Systems Security and Sustainability WS4 2023 which is divided into different sections such as Smart IT Infrastructure for Sustainable Society Smart Management Prospective for Sustainable Society Smart Secure Systems for Next Generation Technologies Smart Trends for Computational Graphics and Image Modeling and Smart Trends for

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Computer System Architecture P. V. S. RAO, 2008-12-30 Intended as a text for undergraduate and postgraduate students of engineering in Computer Science and Engineering Information Technology and students pursuing courses in computer applications BCA MCA and computer science B Sc M Sc this state of the art study acquaints the students with concepts and implementations in computer architectures Though a new title it is a completely reorganized thoroughly revised and fully updated version of the author's earlier book Perspectives in Computer Architecture The text begins with a brief account of the very early history of computers and describes the von Neumann IAS type of computers then it goes on to give a brief introduction to the subsequent advances in computer systems covering device technologies operational aspects system organization and applications This is followed by an analysis of the advances and innovations that have taken place in these areas Advanced concepts such as look ahead pipelining RISC architectures and multi programming are fully analyzed The text concludes with a discussion on such topical subjects as computer networks microprocessors and microcomputers microprocessor families Intel Pentium series and newer high power processors

HALLMARKS OF THE BOOK The text fully reflects Professor P V S Rao's long experience as an eminent academic and his professional experience as an adviser to leading telecommunications software companies Gives a systematic account of the evolution of computers Provides a large number of exercises to drill the students in self study The five Appendices at the end of the text cover the basic concepts to enable the students to have a better understanding of the subject Besides students practising engineers should also find this book to be of immense value to them

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