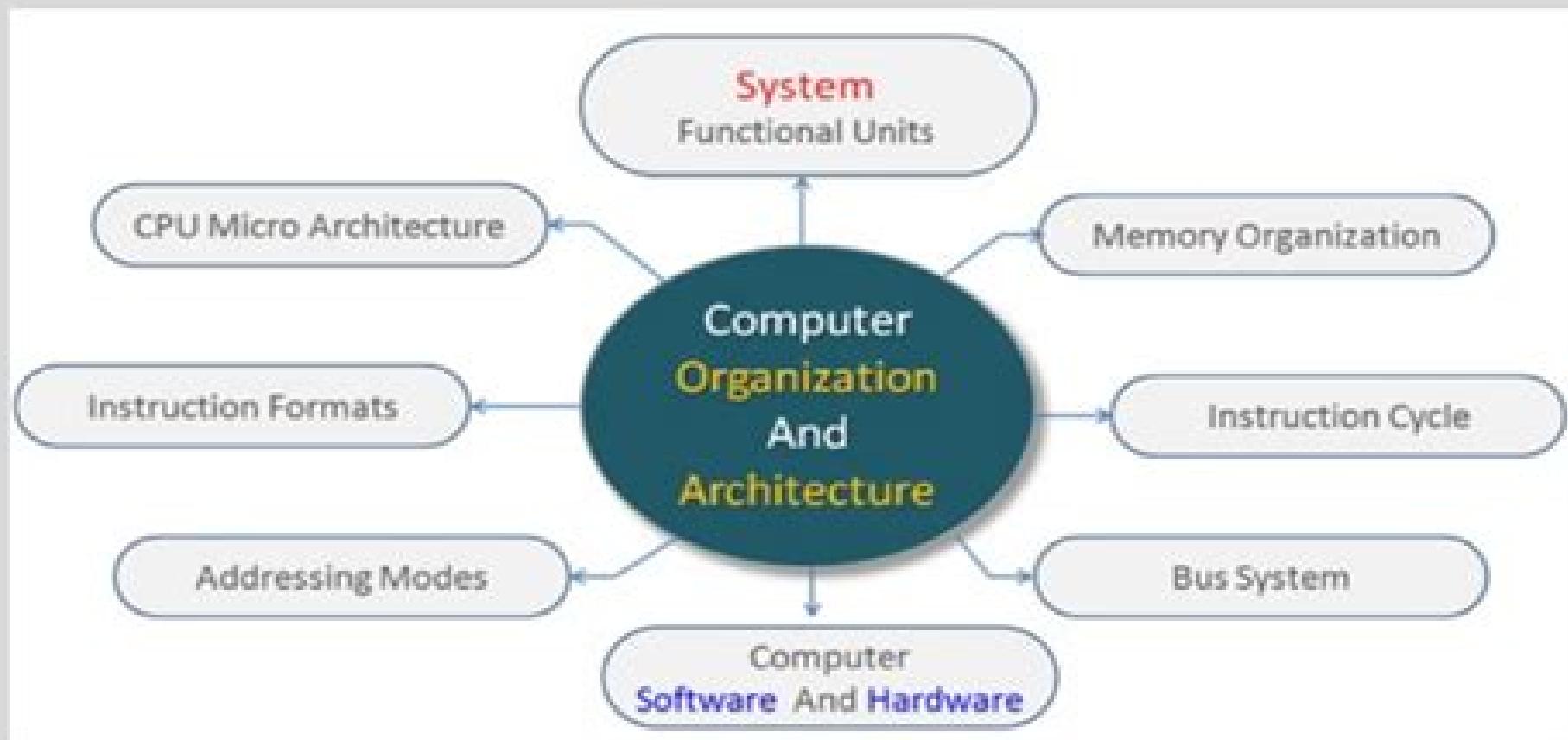


# Organization and Architecture



# Computer Organization And Architecture Glossary

**Cybellium**

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Turing Award recognizing contributions of lasting and major technical importance to the computing field is fully revised with the latest developments in processor and system architecture. The text now features examples from the RISC V RISC Five instruction set architecture a modern RISC instruction set developed and designed to be a free and openly adoptable standard. It also includes a new chapter on domain specific architectures and an updated chapter on warehouse scale computing that features the first public information on Google's newest WSC. True to its original mission of demystifying computer architecture this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening while always keeping an emphasis on good engineering design. Winner of a 2019 Textbook Excellence Award Texty from the Textbook and Academic Authors Association. Includes a new chapter on domain specific architectures explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore's Law and Dennard scaling. Features the first publication of several DSAs from industry. Features extensive updates to the chapter on warehouse scale computing with the first public information on the newest Google WSC. Offers updates to other chapters including new material dealing with the use of stacked DRAM data on the performance of new NVIDIA Pascal GPU vs new AVX 512 Intel Skylake CPU and extensive additions to content covering multicore architecture and organization. Includes Putting It All Together sections near the end of every chapter providing real world technology examples that demonstrate the principles covered in each chapter. Includes review appendices in the printed text and additional reference appendices available online. Includes updated and improved case studies and exercises. ACM named John L. Hennessy and David A. Patterson recipients of the 2017 ACM A.M. Turing Award for pioneering a systematic quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry.

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